EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	9017	content near2 addressable near2 memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:01
S2	768102	S1 CAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2007/10/23 19:02
S3	252	S2 and ((physical real) same (logical virtual) same bank)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2007/10/23 19:04
S4	13	S3 and (711/108 365/49).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:10
S5	153	(physical real) same (logical virtual) same bank same assign\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2007/10/23 19:09
S6	2	S5 and (711/108 365/49).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:06
S7 .	. 25	(physical real) with (logical virtual) with bank with assign\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:05
S8	12	S2 and (physical real) same (logical virtual) same bank same assign\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:06

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S9	75	CAS and (physical real) same (logical virtual) same bank same assign\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:06
S10	1	S9 and (711/108 365/49).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:06
S11	20	S9 and "711".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:10
S12	281	(convert\$4 conversion) same (physical real) same (logical virtual) same bank	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:09
S13	90	(convert\$4 conversion) same (physical real) same (logical virtual) same bank and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:10
514	2	S12 and (711/108 365/49).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:10
S15	5	S13 and "711".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:10
S16	. 0	S13 and "365".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:12

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S17	12	("5475825" "5860145" "5946716" "6047354" "6079004" "6138225" "6212613" "6339816" "6374341" "6418521" "6535528" "6662289").PN. OR ("7024536").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/10/23 19:11
S18	476	cascad\$4 and S2 and (physical real) and (logical virtual) and bank	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:13
S19	106	cascad\$4 and S2 and ((physical real) same (logical virtual) same bank)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:14
S20		S19 and ("711" "365").clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/23 19:17
S21	60	("3648254" "4996666" "5956336" "5978885" "6041389" "6069573" "6081440").PN. OR ("6374326").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/10/23 19:15
S22	2431	(711/108,205,207 365/49).ccls.	USPAT	OR	ON	2007/10/24 11:51

Search EAST Search History Interference

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	674	(711/108,205,207 365/49).ccls.	US-PGPUB	OR	ON	2007/10/24 12:21
L2	0	L1 and ("logical bank-physical bank converter").clm.	US-PGPUB	OR	ON	2007/10/24 12:23
L3 .	1	L1 and ("physical bank converter"). clm.	US-PGPUB	OR	ON	2007/10/24 12:22
L4	. 1	L1 and ("no physical bank assigned").clm.	US-PGPUB	OR	ON	2007/10/24 12:23
L5	1	L1 and ("no hit entry").clm.	US-PGPUB	OR	ON	2007/10/24 12:23
L6	1	L1 and ("no empty entry").clm.	US-PGPUB	OR	ON	2007/10/24 12:23
L7	1	L1 and ("logical bank" with "physical bank" with converter).clm.	US-PGPUB	OR	ON	2007/10/24 12:23

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Bank conflict determination - Patent 20030163643

18, 2000, and co-pending and commonly **assigned** U.S. patent application Ser. No. **Logical** AND gate 756 is included to output whether entry B is **bank** ... www.freepatentsonline.com/20030163643.html - 95k - <u>Cached</u> - <u>Similar pages</u> [More results from www.freepatentsonline.com]

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Idiom recognizer within a register alias table - US Patent 5471633 a size comparator, coupled to said high bank array and said low bank array, for comparing said logical register sizes to said high bank physical register ... www.patentstorm.us/patents/5471633-claims.html - 49k - Cached - Similar pages [More results from www.patentstorm.us]

Linux version 2.4.20-30.9 (bhcompile@porky.devel.redhat.com) (gcc ... 82801BAM/CAM PCI Bridge PCI: Using IRQ router PIIX [8086/248c] at 00:1f.0 Memory physical id: b slot: System board or motherboard capacity: 1MB *-bank ... rad.spc.org/datamachine.txt - 19k - Cached - Similar pages

Memory Organization

Bank switching assigns different units to parts of memory likely to be used for ... When the CAM is signalled to perform a search, the logic associated with ... www.csee.wvu.edu/~jdm/classes/cs455/notes/tech/memorg.html - 12k - Cached - Similar pages

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